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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/080,933	02/22/2002	Jianzhong Zhang	059864.00665	6502
	7590 09/30/200 DERS & DEMPSEY L	EXAMINER		
8000 TOWERS CRESCENT DRIVE 14TH FLOOR			CORRIELUS, JEAN B	
VENNA, VA 22182-6212			ART UNIT	PAPER NUMBER
			2611	
			MAIL DATE	DELIVERY MODE
			09/30/2009	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)		
	10/080,933	ZHANG ET AL.		
Office Action Summary	Examiner	Art Unit		
	Jean B. Corrielus	2611		
The MAILING DATE of this communication appeariod for Reply	pears on the cover sheet with the c	correspondence address		
A SHORTENED STATUTORY PERIOD FOR REPL WHICHEVER IS LONGER, FROM THE MAILING D - Extensions of time may be available under the provisions of 37 CFR 1.7 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailin earned patent term adjustment. See 37 CFR 1.704(b).	NATE OF THIS COMMUNICATION 136(a). In no event, however, may a reply be tirwill apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).		
Status				
Responsive to communication(s) filed on <u>9/3/(</u> This action is FINAL . 2b) ☑ This Since this application is in condition for alloware closed in accordance with the practice under the practice under the practice.	s action is non-final. ince except for formal matters, pro			
Disposition of Claims				
4) ☐ Claim(s) 21,23-28,30-33,36-38,40-42,46 and 4 4a) Of the above claim(s) is/are withdra 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 21,23-28,30-33,36-38, and 40-42, 46 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	wn from consideration. 6-47 is/are rejected.	n.		
Application Papers				
9) ☐ The specification is objected to by the Examine 10) ☑ The drawing(s) filed on 15 August 2006 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) ☐ The oath or declaration is objected to by the Example 2.	a) accepted or b) dobjected or b) dobjected or drawing(s) be held in abeyance. Section is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).		
Priority under 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 				
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D: 5) Notice of Informal F 6) Other:	ate		

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DETAILED ACTION

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 9/3/09 has been entered.

Drawings

2. The drawings are objected to under 37 CFR 1.83(a) because they fail to show a signal input to the "joint optimizer", shown in fig. 3 (as described in the specification page 8, lines 15-16, fig. 3 is a block diagram of a portion of the receiving station that forms part of the system shown in fig. 1. Since fig. 3 is a block diagram of a receiving portion of fig. 1, it is not clear how the optimizer 74 shown in fig. 3 is to be connected to existing component(s) of fig. 1 to receive its input). Any structural detail that is essential for a proper understanding of the disclosed invention should be shown in the drawing.

MPEP § 608.02(d). Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the

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replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

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3. The drawings were received on 9/3/09. These drawings are <u>not acceptable</u> because there is no support in the specification as filed for the new drawing structure. More specifically, with respect to fig. 2, the joint optimizer receiving four input signals and providing three output signals is not supported by the original disclosure. In addition, with respect to fig. 3, the structure of the optimizer receiving two input signals and providing three output signals is not supported by the original disclosure. Examiner notes that fig. 2 and 3 are described as two distinct embodiments of a portion of the receiving device shown in fig. 1, as indicated as page 8, lines 13-16. Because both figures 2 and 3 are described as distinct embodiments of a portion of the receiving station shown in fig. 1, the drawing objection with respect <u>only</u> to the inconsistency between fig. 2 and 3 has been withdrawn, since fig.2 and fig. 3 are not described to be related.

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4. Examiner also notes that the amendment is not in accordance with 37 CFR 1.121(d) that states that any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. In the immediate prior version of the drawing sheets, both figs. 2 and 3 were presented in separate sheets. Accordingly, the replacement sheets have to be presented separately as well.

Examiner's comment

5. Note that claims 38, 40-42 recite means plus function limitations and effectively invoke 112 sixth paragraph. The claims are treated as such.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 7. Claims 21, 23-26, 28, 30, 38, 40-42 and 46-47 are rejected under 35 U.S.C. 102(e) as being anticipated by Zangi et al US patent No. 6,775,322 et al.

As per claim 21, Zangi et al teaches a receiving station (figs. 1 and 3) comprising a signal filter see col. 3, lines 47-50 inherently in communication with a signal receiving antenna (note fig. 1 is described by Zangi as see col. 3, lines 29-30, as a mobile station therefore it has to include an antenna); a signal estimator 122 in communication with the

signal filter see col. 4, lines 57-60; circuit (124) corresponding to the claimed (signal optimizer) configured to generate tap coefficients (optimized values) for the signal from the signal filter; a prefilter 102 configured to filter the signal from the signal filter using the generated tap coefficients (optimized values) for the signal see col. 4, lines 59-61; circuits (104, 106 and 108) considered as the claimed "decision feedback sequence estimator" to receive the coefficients (optimized values) note input to filter 104, circuit blocks (104, 106 and 108) "decision feedback sequence estimator" comprising a summing element 106, a feedback filter 104 and a maximum likelihood sequence estimator 108, see col. 11, lines 9-12, as shown in fig. 3, Zangi teaches that the summing element 106, the feedback filter 104 and the MLSE 108 are operatively connected to one another and further connected to prefilter 102. Note that the interconnection of the prefilter 102r, the feedback filter 104, the MLSE 108 and the summing element 106 cooperatively operate to permit inherently concurrent interference and prefilter operation to be performed because there is no structural difference between the Zangi's disclosed features of prefilter, the feedback filter, the MLSE and the summing element and the applicant claimed features of "prefilter, the feedback filter, the MLSE and the summing element".

As per claim 23, Zangi et al teaches that the output of the decision device (MLSE) 108 is configured to transmit generated maximum likelihood values through an output to the feedback filter 104 and the input of the decision device (MLSE) 108 is configured to receive summed values from the summing element 106.

As per claim 24, Zangi et al teaches the feedback filter 104 comprises a first input in communication with circuit 124 (signal optimizer) and configure to receive the optimized values from the circuit 124 (signal optimizer) and a second input configured to receive the generated maximum likelihood values from the MLSE 108.

As per claim 25, Zangi et al further teaches the summing element 106 receives inputs from prefilter 102 and the feedback filter 104 and sends a summed output to the MLSE device 108.

As per claim 26, the signal filter see col. 3, lines 47-50 is located in the forward path of the receiving station hence it has to be a feedforward filter.

As per claim 28, Zangi further teaches that the feedback filter 104 receives optimized signals from the signal optimizer 124 that are used to define filter characteristics of the feedback filter 104 see col. 4, lines 57-60.

As per claim 30, the signal filter see col. 3, lines 47-50 and the signal estimator 122 is placed in the received chain of the receiving station see fig. 1.

As per claim 38, Zangi et al teaches a receiving station (fig. 1 and 3) comprising see col. 3, lines 47-50 inherently in communication with a signal receiving antenna (note fig. 1 is described by Zangi as see col. 3, lines 29-30, as a mobile station therefore it has to include an antenna); a signal estimating means 122 for estimating channel operations of the signal from the signal filtering means; means 124 corresponding to the claimed signal optimizing means in communication with the signal filtering means for generating coefficients (optimized values); prefiltering means 102 for filtering the signal from the signal filtering means using the generated coefficients (optimized values)

means (104, 106 and 108) considered as the claimed "interference cancellation means" for receiving the coefficients (optimized values) to perform concurrent interference and prefilter operations; Zangi further teaches that means (104, 106 and 108) (interference canceling means) comprises summing means 106 for summing inputs from the prefiltering means 102; MLSE means 108 for generating maximum values from the summing means 106; and feedback filtering means 104 for filtering an output of the MLSE 104 based on the generated optimized values to generate feedback-filtered values. Note that the interconnection of the prefiltering means 102, the feedback filtering means 104, the MLSE means 108 and the summing means 106 cooperatively operate to permit inherently concurrent interference and prefilter operation to be performed because there is no structural difference between the Zangi's claimed features of prefiltering means, the feedback filtering means, the MLSE means and the summing means and the applicant claimed features of "prefiltering means, the feedback filtering means.

As per claim 40 see claim 23.

As per claim 41 see claim 24.

As per claim 42, Zangi et al further teaches the summing element 106 receives inputs from prefilter 102 and the feedback filter 104 and sends a summed output to the MLSE device 108 and an output of the MLSE being an output from the receiving station see fig. 3.

As per claim 46 the apparatus is a mobile communication device. See col. 3, lines 29-30.

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As per claim 47 the device is inherently an integrated circuit because mobile communication devices uses IC circuit.

Claim Rejections - 35 USC § 103

- 8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 9. Claim 27 is rejected under 35 U.S.C. 103(a) as being unpatentable over Zangi et al US patent No. 6,775,322 et al. in view of Taylor US Patent Application No. 2002/0197987.

As per claim 27, as applied to claim 25 above, Zangi et al teaches every feature of the claimed invention but does not explicitly teach the further limitation of a deinterleaver in communication with an output of the MLSE estimator and depuncture in communication with a deinterleaver and a channel decoder in communication with the deinterleaver. Taylor et al teaches a deinterleaver 58 in communication with an output of the MLSE estimator (i.e. output of demodulator/equalizer 56) and depuncture 62 in communication with a deinterleaver 58 and a channel decoder 64 in communication with the deinterleaver 58. It would have been obvious to one skill in the art to incorporate such a teaching in Zangi et al in order to recover the originally transmitted signal.

10. Claims 31-33 and 36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zangi et al US patent No. 6,775,322 in view of Malkemes et al US Patent Application publication S/N US2002/0106040 A1.

As per claim 31, as applied to claim 30 above, Zangi et al teaches every feature of the claimed invention but does not explicitly teach that the receiving station comprises a plurality of receive chains that corresponds to a plurality of signal receiving antennas configured to receive and transmit a plurality of signal vector to the plurality of receive chains. Malkemes et al teaches the receiving station (fig. 1) comprises a plurality of receive chains see fig. 1) that corresponds to a plurality of signal receiving antennas 102 configured to receive and transmit a plurality of signal vector to the plurality of receive chains. Given that fact, it would have been obvious to one skill in the art to incorporate such a teaching in Zangi et al in order to improve signal detection since the system would have been able to be configured to receive multiple copies so that existence of signal error can be easily determined.

As per claim 32, see claim 31. In addition, Zangi teaches transmitting the coefficients (optimized feed forward filter parameters and the optimized feedback filter parameters) to a decision feedback sequence estimator (104, 106 and 108), wherein the decision feedback sequence estimator (104, 106 and 108) comprises a feedback filter 104: note that the limitation "simultaneously" is interpreted as "both". Clearly Zangi teaches that "both" interference cancellation and prefiltering operations are performed via the feedforward filter 102 and the feedback filter 104. see col. 4, lines 43-50. In addition, for the sake of argument, note that the prefiltered signal from feedforward filter

102 is provided as input to the summer 106 at the same time as the ISI compensated signal generated by feedback filter 104 (see col. 7, lines 15-21) another indication that the prefilter and ISI compensation are performed simultaneously.

As per claim 33, Zangi et al further teaches the feedforward filter 102 filters the data vector and transmitting a feedforward output to a summing element 106; receiving an output of the summing element in a MLSE device 108 and generating an output of that is transmitted to an input of the feedback filter 104 and subsequent component and filtering the output received from the MSLE device in the feedback filter 104 and transmitting a filtered signal to the summing element 106.

As per claim 36, Zangi further teaches the received chain comprises a receiving filter see col. 3, lines 47-50 inherently in communication with a signal receiving antenna (note fig. 1 is described by Zangi as see col. 3, lines 29-30, as a mobile station therefore it has to include an antenna); a channel estimator 122 in communication with the receiving filter; the channel estimator 122 in communication with circuit 124 corresponding to the claimed signal optimizer configured to optimized feedforward and feedback filter parameters see col. 5, lines 1-27.

11. Claim 37 is rejected under 35 U.S.C. 103(a) as being unpatentable over Zangi et al US patent No. 6,775,322 in view of Malkemes et al US Patent Application publication S/N US2002/0106040 A1 and further in view of Taylor US Patent Application No. 2002/0197987.

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As per claim 37, as applied to claim 33 above, Zangi et al and Malkemes et al teach every feature of the claimed invention but do not explicitly teach the further limitation of a deinterleaver in communication with an output of the MLSE estimator and depuncture in communication with a deinterleaver and a channel decoder in communication with the deinterleaver. Taylor et al teaches a deinterleaver 58 in communication with an output of the MLSE estimator (i.e. output of demodulator/equalizer 56) and depuncture 62 in communication with a deinterleaver 58 and a channel decoder 64 in communication with the deinterleaver 58. It would have been obvious to one skill in the art to incorporate such a teaching in Zangi et al and Malkemes in order to recover the originally transmitted signal.

Response to Arguments

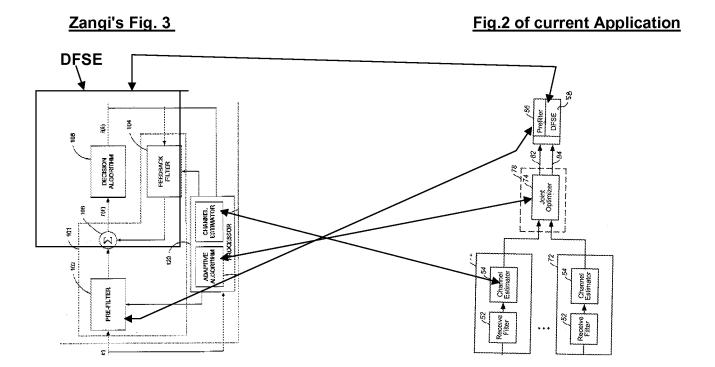
12. Applicant's arguments filed 9/3/09 have been fully considered but they are not persuasive. Applicant argues that the drawings as amended are supported by the specification at least by sections of pages 12-16. However, a review of the entire specification, and specifically the section noted by applicant, fails to support the applicant's position. As noted above under the "drawings" subheading, with respect to fig. 2, the joint optimizer receiving four input signals and providing three output signals is not supported by the original disclosure. In addition, with respect to fig. 3, the structure of the optimizer receiving two input signals and providing three output signals is not supported by the original disclosure. Examiner notes the specification makes it clear that with respect to fig. 2, the optimizer 74 receives estimated values from the channel

estimator (page 12, lines 7-8) and provides optimized parameter values on line 82 and 84 (page 12, line 17) to prefilter and DFSE 56/58 (page 12, lines 20-21) and with respect to fig. 3, the specification at page 13, lines 1-8 makes it clear that the optimizer 74 provides optimized parameter values are provided on lines 82 and 84 to prefilter 56 and feedback filter 92, respectively, values of the data signal filtered by **a prefilter is provided on line 53 to the prefilter 56**. Examiner also notes that fig. 2 and 3 are disclosed as **two distinct embodiments of a portion of the receiving device** shown in fig. 1, see the specification page 8, lines 13-16.

Applicant further submits that Zangi fails to disclose or suggest each and every element recited in claims 21 and 38. Specifically, Zangi fails to disclose or suggest, at least, "a decision feedback sequence estimator configured to receive the generated optimized values, wherein the decision feedback sequence estimator comprises a summing element, a feedback filter, and a maximum likelihood sequence estimator," as recited in claim 21 (emphasis added), and similarly recited in claim 38. However it is noted that such point of arguments have been previoulsly presented by applicant in the response filed on 11/19/08 and rebutted by the examiner in the office action mailed on 12/19/08. Please refer to said office action for the examiner's rebuttal. In addition examiner maintains that at least Fig. 3 of Zangi shows the corresponding claim features of applicant's invention. As shown in the diagram below, each of applicant's claimed feature, shown, for instance in fig. 2, corresponds one to one to each of Zangi's components shown at least in fig. 3. The DFSE circuit of Zangi (see diagram below), corresponding to applicant's claimed

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limitation "DFSE", includes a summer 106, a feedback filter 104 and a maximum likelihood sequence estimator 108. in addition, the adaptive algorithm 124 of zangi corresponds to signal optimizer, shown in applicant's fig. 2, as block 74. Zangi further teaches a channel estimator 122, shown in applicant's fig. 2 as 54. Zangi teaches a prefilter 102 corresponding to applicant's circuit block 56 depicted in fig. 2. Since Zangi teaches every feature of the claimed invention, i.e., structurally and functionnaly, as depicted in the above rejection, and further, as shown in the mapping below, zangi anticipates the claimed invention.



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Applicant's argument(s) with respect to the Taylor's and Makemes' references is moot since the limitations allegedly missing in Zangi et al are in fact present, as set forth in the above comment and rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean B. Corrielus whose telephone number is 571-272-3020. The examiner can normally be reached on Monday-Thursday from 9:30-3:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh Fan can be reached on 571-272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Jean B Corrielus/ Primary Examiner, Art Unit 2611